

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Christophe Chevallier

Attorney Docket No.: 400.184US05

Title: IDENTIFYING LINE WIDTH ERRORS IN INTEGRATED CIRCUIT DESIGNS

MS Patent Application  
 Commissioner for Patents  
 P. O. Box 1450  
 Alexandria, VA 22313-1450

We are transmitting the following documents along with this Transmittal Sheet:

☒ Incorporate by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*

☒ **DIVISIONAL** of prior Patent Application Serial No. 10/199,727 filed July 19, 2002 which is incorporated herein by reference and claims priority under 37 CFR 1.53(b) comprising:

☒ Specification 15 pgs., including claims numbered 1-18 and a 1 page abstract;

☒ 4 Sheets of Formal drawings;

☒ a copy of the Declaration from prior application (3 pgs.);

☒ a copy of Power of Attorney from prior application (1 pg.);

☒ Prior application is assigned of record to: Micron Technology, Inc.;

☒ a Communication Regarding Divisional Application (2 pgs.);

☒ Information Disclosure Statement (1 pg.) and Form 1449 (1 pg.) -- References not enclosed, cited in prior application;

☒ a check in the amount of \$770.00 to cover the filing fee, which is calculated below:

**PLEASE CHARGE FOR ANY ADDITIONAL FEES OR CREDIT OVERPAYMENT TO  
 DEPOSIT ACCOUNT 501373**

**CUSTOMER NUMBER 27073**

APPLICATION FILING FEE					
	Number of Claims Filed (1)	Claims Included in Basic Filing Fee (2)	Number of Extra Claims (1-2)	Cost per Extra Claim	Fee Required
Total Claims	18	- 20 =	0	x \$18 =	\$
Independent Claims	3	- 3 =	0	x \$86 =	\$
One or More Multiple Dependent Claims Presented? If Yes, Enter \$290 Here *					\$
Enter Basic Filing Fee (Utility Patent-\$770.00) Here *					\$ 770.00
<b>Total Application Filing Fee</b>					<b>\$ 770.00</b>

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**Express Mail" mailing label number: EV372994168US Date of Deposit: February 19, 2004**

These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to: MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)

First Named Inventor	Christophe Chevallier	<b><u>COMMUNICATION</u></b> <b><u>REGARDING DIVISIONAL</u></b> <b><u>APPLICATION</u></b>
Serial No.	Unknown	
Filing Date	Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.184US05	
Title: IDENTIFYING LINE WIDTH ERRORS IN INTEGRATED CIRCUIT DESIGNS		

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Prior to taking the above-entitled patent application under examination, please review the following comments:

#### **COMMENTS**

This is a divisional application of application Serial No. 10/199,727 (the '727 application), titled LINE WIDTH CHECK IN LAYOUT DATABASE, filed July 19, 2002 (pending). The '727 application was subjected to a Restriction Requirement by Examiner Brandon Bowers in the Office Action mailed August 26, 2003. Non-elected claims 11, 12, 13, and 14 of the '727 application respectively correspond to claims 1, 2, 3, and 11 of the present divisional application. Applicant contends that the remaining claims of the present divisional application comport with the subject matter of non-elected claims 11, 12, 13, and 14 of the '727 application.

The title has been amended to conform the title to the claims. The specification has been amended to add a cross reference to the '727 application and to conform the Summary to the claims. The Abstract has been amended to conform it to the claims. Formal drawings are submitted.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at 612 312-2208.

Respectfully submitted,

Date: 02-19-2004

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